

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Michitaro KANAMITSU et al.

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR DEVICE AND DATA PROCESSING SYSTEM

* * *

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

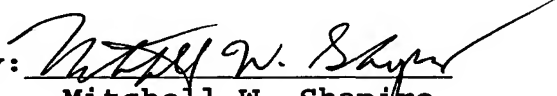
Applicants wish to make of record the documents cited in predecessor Application No. 10/013,538 filed December 13, 2001, whether cited by Applicants or by the Patent Office. The documents are listed on the attached Form-1449.

Respectfully submitted,

MWS:sjk

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July 22, 2003

FORM PTO-1449				Atty. Docket No. XA-9592		Appln. No.	
<u>LIST OF DOCUMENTS CITED BY APPLICANT</u>				Applicant Michitaro KANAMITSU et al.			
				Filing Date HEREWITH		Group	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA	6,078,519	06/20/00	Kanamitsu et al.	365	185.03	
	AB	6,046,936	04/04/00	Tsujikawa et al.	365	185.03	
	AC	6,125,052	09/26/00	Tanaka	365	185.03	
	AD	6,097,634	08/01/00	Sugiyama	365	185.21	
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AL	11-232886	8/27/99	JAPAN			No
	AM	11-345494	12/14/99	JAPAN			No
	AN						
	AO						
	AP						
	AQ						
OTHER (including author, title, date, pertinent pages, etc.)							
	AR						
Examiner				Date Considered			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							